


<b>ARK   Compare Intel® Products</b>	
<b>Intel® Xeon® Processor E5-2670 v3 (30M Cache, 2.30 GHz)</b>	
<b>Essentials</b>	
Product Collection	Intel® Xeon® Processor E5 v3 Family
Code Name	Products formerly Haswell
Vertical Segment	Server
Processor Number	E5-2670V3
Status	Discontinued
Launch Date	Q3'14
Lithography	22 nm
Recommended Customer Price	\$1589.00
<b>CPU Specifications</b>	
# of Cores	12
# of Threads	24
Processor Base Frequency	2.30 GHz
Max Turbo Frequency	3.10 GHz
Cache	30 MB Intel® Smart Cache
Bus Speed	9.6 GT/s
# of QPI Links	2
Intel® Turbo Boost Technology 2.0 Frequency‡	3.10 GHz
TDP	120 W
VID Voltage Range	0.65V–1.30V
<b>Supplemental Information</b>	
Embedded Options Available	No
Datasheet	<a href="#">View now</a>
Product Brief	<a href="#">View now</a>
Additional Information	<a href="#">View now</a>
<b>Memory Specifications</b>	
Max Memory Size (dependent on memory type)	768 GB
Memory Types	DDR4 1600/1866/2133
Max # of Memory Channels	4
Max Memory Bandwidth	68 GB/s
Physical Address Extensions	46-bit
ECC Memory Supported ‡	Yes
<b>Expansion Options</b>	

Scalability	2S
PCI Express Revision	3
PCI Express Configurations ‡	x4, x8, x16
Max # of PCI Express Lanes	40
<b>Package Specifications</b>	
Sockets Supported	FCLGA2011-3
Max CPU Configuration	2
TCASE	84.5°C
Package Size	52.5mm x 45mm
<b>Advanced Technologies</b>	
Intel® Turbo Boost Technology ‡	2
Intel vPro® Platform Eligibility ‡	Yes
Intel® Hyper-Threading Technology ‡	Yes
‡	Yes
Intel® Virtualization Technology for Directed I/O (VT-d) ‡	Yes
Intel® VT-x with Extended Page Tables (EPT) ‡	Yes
Intel® Transactional Synchronization Extensions	No
Intel® 64 ‡	Yes
Instruction Set	64-bit
Instruction Set Extensions	Intel® AVX2
Idle States	Yes
Technology	Yes
Intel® Demand Based Switching	Yes
Thermal Monitoring Technologies	Yes
Intel® Flex Memory Access	No
Intel® Identity Protection Technology ‡	No
<b>Security &amp; Reliability</b>	
Intel® AES New Instructions	Yes
Secure Key	Yes
Intel® OS Guard	Yes
Intel® Trusted Execution Technology ‡	Yes
Execute Disable Bit ‡	Yes